

NAL/PUR/ALD/316/22-Z

Date: 20-Feb-2023

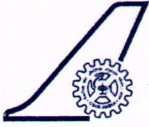
CORRIGENDUM

Tender ID:2022_CSIR143697_1

In continuation to our tender No.NAL/PUR/ALD/316/22-Z, uploaded on 1-Feb-2022, for “ Design, Development and Supply of portable FPGA based interface test card at CSIR-NAL”, please note the following changes:-

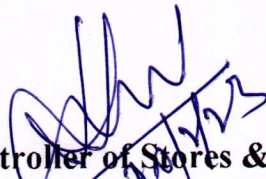
Sr. No.	Query	Clarification
1	The recommended FPGA family with Manufacturer Part no. that can be referred to	Xilinx 7 series of FPGAs (preferably Xilinx Artix 7 series – part no. is to be finalised during detailed design based on resource & pin requirements and related constraints)
2	Do PCBs have to be with Conformal Coating?	No
3	Do need to consider the LAB charges towards EMI/EMC testing.	No – please refer to section 14.4.2 (item (c) in pg. 46 and 47), of the released tender document
4	Scope of Work of Inevitable <ul style="list-style-type: none">• Design, Development and supply of Portable FPGA based interface test card(s) hardware• Electrical testing of all interfaces• Support to CSIR-NAL team during the FPGA IP Cores testing and integration, during acceptance and within warranty period	Yes





5	Out of Scope <ul style="list-style-type: none">• FPGA IP Core design and development is not in the scope of this tender• Design of the chassis/enclosure is not in the scope of this tender	Yes
6	Architecture block diagrams shall be shared with the bidder after placement of the PO, to enable the detailed design.	Yes
7	Should we develop IPs for ARINC-429, ARINC-717, CAN bus, or use available devices from OEMs?	<p>The scope does not include development of IPs. NAL shall be integrating their IP on to the FPGA.</p> <p>Support during integration, if required, is to be provided by the bidder. HW transceivers (ICs) for ARINC-429, ARINC-717, CAN bus are to be provided on the proposed hardware vide this tender.</p>

Kindly, note that, all other terms and conditions of the tender shall remain the same.


Controller of Stores & Purchase
For and on behalf of CSIR